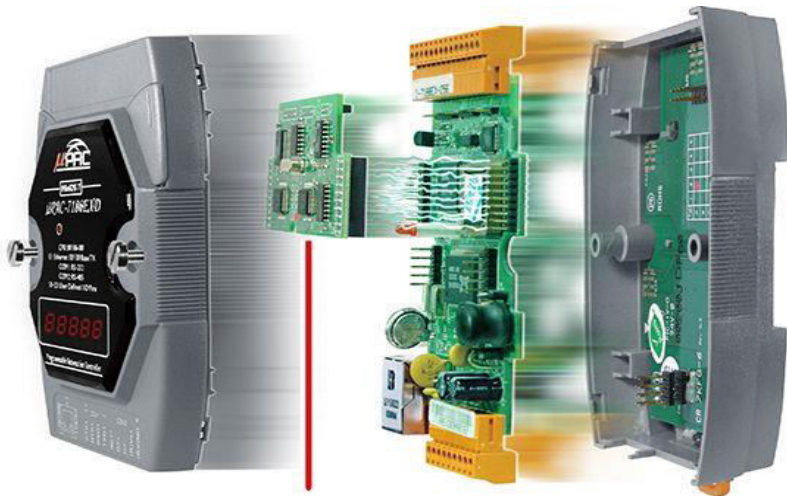
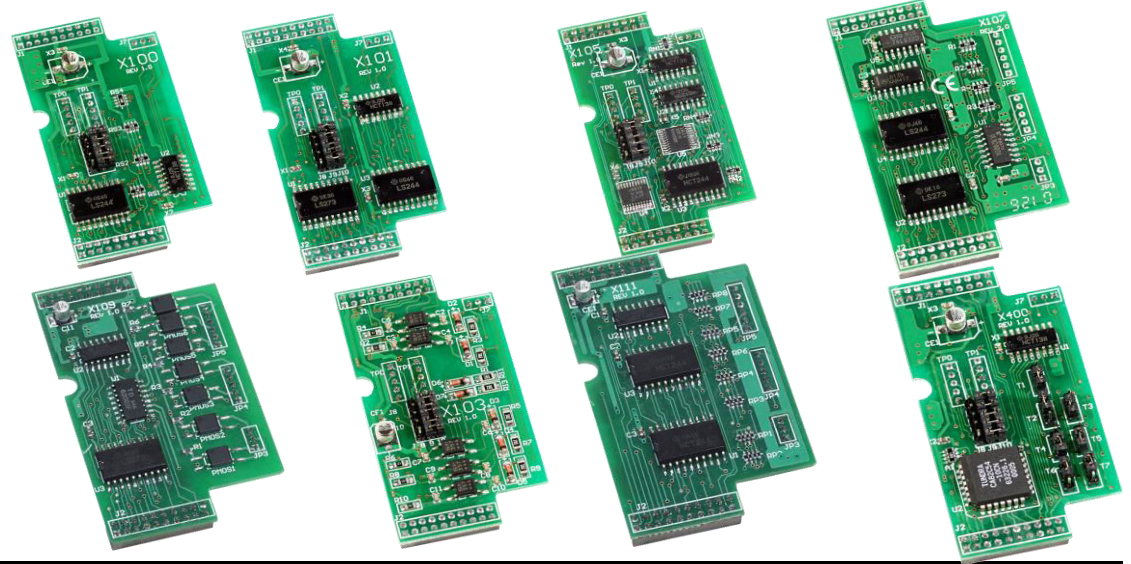


**Digital I/O Expansion Board**



**Optional X-board**



Model	Description	DI	DO	Relay Output	Counter/Timer	Used with I-7188XA/XB/XC/XG/EX/EG
<a href="#">X100</a>	DI	8	-	-	-	XC
<a href="#">X101</a>	DO	-	8	-	-	XC
<a href="#">X102</a>	Relay Output	-	-	2	-	XC
<a href="#">X103</a>	DI	7	-	-	-	XC
<a href="#">X104</a>	DI, DO	8 (each channel can be programmed to DI/DO)		-	-	XC



Model	Description	DI	DO	Relay Output	Counter/Timer	Used with I-7188XA/XB/XC/XG/EX/EG
<a href="#">X105</a>	DI, DO	8 (each channel can be programmed to DI/DO)		-	-	XC
<a href="#">X106</a>	DI, DO	Can be used as 2 channels DO or 3 channels DI		-	-	XC
<a href="#">X107</a>	DI, DO	6	7	-	-	XB/XG/EX/EG/ $\mu$ PAC-7186EG
<a href="#">X109</a>	PhotoMOS	-	-	7	-	XB/XG/EX/EG/ $\mu$ PAC-7186EG
<a href="#">X110</a>	DI	14	-	-	-	XB/XG/EX/EG/ $\mu$ PAC-7186EG
<a href="#">X111</a>	DO	-	13	-	-	XB/XG/EX/EG/ $\mu$ PAC-7186EG
<a href="#">X116</a>	Relay Output	4	-	6	-	XB/XG/EX/EG ** Without Case **
<a href="#">X119</a>	DI, DO	7	7	-	-	XB/XG/EX/EG ** Without Case **
<a href="#">X400</a>	Timer/Counter	-	-	-	3 channels 16-bit timer/counter	XC

**Following  $\mu$ PAC supports I/O Expansion Bus, can mount one X-Board**

- 1) For C language solution: I-7188XB(D), I-7188EX(D),  $\mu$ PAC-7186EX(D),  $\mu$ PAC-7186PEX(D),  $\mu$ PAC-7186EX(D)-FD,  $\mu$ PAC-7186EX(D)-SM
- 2) For ISaGRAF solution: I-7188XG(D),  $\mu$ PAC-7186EG(D)

**X-Board is Series has following common specifications**

- 1) DI channel: Dry contact, sink type, non-isolated
- 2) DO channel: Open Collector, sink type, 100 mA/channel load current, non-isolated